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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



Applicant: GUNTER, Liberty L. et al. Group Art Unit: 2823
Serial No. 10/532,456 Examiner: COLEMAN, William D.
Filing or 371 (c) Date: 04/22/2005 Atty. Dkt. No: 20030213-US

For: GaN-BASED PERMEABLE BASE TRANSISTOR AND METHOD OF
FABRICATION

To: Mail Stop Amendment
Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

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Transmitted herewith is the Applicant's Information Disclosure Statement, 2 sheets form PTO/SB/08A and/or PTO/SB/08B, and 7 references and other information. Pursuant to 37 CFR 1.98 (a)(2)(i) applicant has not transmitted herewith copies of cited U.S. Patents and U.S. patent application publications as the above application was filed after June 30, 2003. Please enter in the above application and communicate in all related matters with the undersigned.

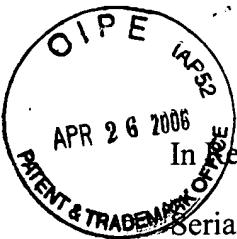
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Respectfully submitted,

Scott J. Asmus
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INFORMATION DISCLOSURE STATEMENT

Applicants submit this statement and the attached 2 sheet(s) of form PTO/SB/08A and/or PTO/SB/08B and 7 references and other information in accordance to the duty of disclosure under 37 C.F.R. §§1.56, 1.97, and 1.98, and requests consideration hereof. Pursuant to 37 CFR 1.98 (a)(2)(i) applicant has not transmitted herewith copies of cited U.S. Patents and U.S. patent application publications as the above application was filed after June 30, 2003.

Compliance with 37 C.F.R. §1.97: This Information Disclosure Statement is filed within three (3) months of the filing date of a National Application or before the mailing date of a first office action on the merits. No fee or certification is required.

Information Cited: The Applicants hereby make of record in the above-identified application, the information listed on the attached form PTO/SB/08A and/or PTO/SB/08B. The order of presentation of the references should not be construed as an indication of the importance of the reference. As all the references listed on attached Form PTO/SB/08A and/or PTO/SB/08B are in English, no commentary is required.

Remarks: A copy of each reference, together with a listing on Form PTO/SB/08A and/or PTO/SB/08B, is submitted herewith. Applicants respectfully request that:

1. The Examiner consider completely the cited information, along with any other information, in reaching a determination concerning the patentability of the present claims;

2. The enclosed form PTO/SB/08A and/or PTO/SB/08B be signed by the Examiner to evidence that the cited information has been fully considered by the Patent and Trademark Office during the examination of this application; and

3. The citations for the information be printed on any patent which issues from this application.

By submitting this Information Disclosure Statement, the Applicants make no representation that a search has been performed, of the extent of any search performed, or that more relevant information does not exist.

By submitting this Information Disclosure Statement, the Applicants make no representation that the information cited in the Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. §1.56(b).

By submitting this Information Disclosure Statement, the Applicants make no representation that the information cited in the Statement is, or is considered to be, in fact, prior art as defined in 37 C.F.R. §102.

Notwithstanding any statement by the Applicant, the Examiner is urged to form his own conclusion regarding the relevance of the cited information.

An early and favorable action is hereby requested.

Respectfully submitted,



Scott J. Asmus, Reg. No. 42,269
Andrew P. Cernota, Reg. No. 52,711
Kristina M. Grasso, Reg. No. 39,205
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Substitute for form 1449/PTC

APR 26 2006

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet 1

of 2

Complete if Known

Application Number	10/532,456
Filing Date	04/22/2005
First Named Inventor	GUNTER, Liberty L. et al.
Art Unit	2823
Examiner Name	COLEMAN, William D.

Case Number: 20030213-US

U. S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature		Date Considered	
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This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449/PTO				Complete if Known	
				Application Number	10/532,456
				Filing Date	04/22/2005
				First Named Inventor	GUNTER, Liberty L. et al.
				Art Unit	2823
				Examiner Name	COLEMAN, William D.
Sheet	2	of	2	Attorney Docket Number	20030213-US

NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.				T ²
		PCT International Search Report dated April 7, 2005 of International Application No. PCT/US04/32276 filed October 1, 2004.				
		NISHIZAWA, JUN-ICHI et al., "The 2.45 GHz 36 W CW Si Recessed Gate Type SIT with High Gain and High Voltage Operation", IEEE Transactions On Electronic Devices, February 2000, pp. 482-487, Vol. 47, No. 2.				
		HENNING, J.P. et al., "A Novel Self-Aligned Fabrication Process for Microwave Static Induction Transistors in Silicon Carbide", IEEE Electron Device Letters, December 2000, pp. 578-580, Vol. 21, No. 12.				
		BUNEA, GABRIELA E. et al., "Modeling of a GaN Based Static Induction Transistor", Dept. of Physics, Electrical and Computer Engineering, Boston University, pp. 1-6, Boston, MA.				
		CAMARCHIA, VITTORIO et al. "Physics-Based Modeling of Submicron GaN Permeable Base Transistors", IEEE Electron Device Letters, June 2002, pp. 303-305, Vol. 23, No. 6.				
		MISRA M. et al. "Investigation of vertical transport in n-GaN films grown by molecular beam epitaxy using Schottky barrier diodes", Applied Physics Letters, Feb. 21, 2000, pp. 1045-1047, Vol. 76, No. 8, American Institute of Physics.				
		BOZLER, CARL O. et al. "Fabrication and Numerical Simulation of the Permeable Base Transistor", IEEE, 1980, pp. 1-14.				

Examiner Signature	Date Considered
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